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Patent us6304103 - fpga using ram control signal

FPGA using RAM control signal lines as routing or logic resources after configuration US 6304103 B1.

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Patent us6157211 - fpga using ram control signal

FPGA using RAM control signal lines as routing or logic resources after configuration US 6157211 A. "Semiconductor Memories", Betty Prince, Wiley 1991. 2)

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